R E J A V E R I O N

Test Engineer since 2018

What I do for a living…

I'm in semiconductors and I work as a test engineer. I develop test solutions for chip companies and make testing cheaper for them.

Here's a list of buzzwords and technical jargon that represent my day-to-day at work:

- ATE Test Development
    - SPEA, LTX MX, ETS88, Teradyne uFLEX, Advantest T2000)
- Mixed-signal and Serial-link products
- Test Data Analytics, Gage R&R, yield improvement
- Altium, Cadence Allegro, LTSpice
- Bench verification
- Quality Assurance, 8D Analysis, 3x5 Why analysis, Failure Mode Effect and Analysis
- Design-for-Test, BIST, ATPG
- C++, Python, JS, VBA
- JIRA, Git, SVN
- Microcontrollers
- Machine Learning, AI

My career so far…

SilTest Semiconductors GmbH - Dortmund, Germany - 01.07.2023 – Present
Test Engineer

- Optimize and increase throughput of volume-runner test solutions
- Mentored and worked with a team of fresh, talented engineers in qualifying and repairing test hardware (probecards, DUT boards, adapterboards) on various tester platforms (Advantest T2000, LTX MX, and SZ Piranha)

Manila Finest Technical Services - Dubai, United Arab Emirates - 18.02.2023 – 30.06.2023
Project engineer who wore many hats

- Developed document generation webtool that streamlined creation of invoices, quotations, statement of account, payroll, and other documents
- Developed the company website
- Coordinated materials procurement and project execution. Did on-site supervision to ensure quality of work and safety of personnel
- Designed 2D/3D proposals using AutoCAD and Photoshop

Analog Devices, Inc. - Cavite, Philippines - 01.02.2018 – 17.02.2023
Senior Engineer

- Full test development
- Designed, characterized, debugged high-bandwidth PCB's that meet GMSL, oLDI LVDS, MIPI, EDP, HDMI signal integrity and power integrity requirements.
- Developed fast and efficient ATE programs without compromising test coverage.
- Collaborated with international cross-functional teams on high-visibility projects with aggressive cycle times.
- Travelled to offshore sites for various engineering/manufacturing purposes.
- Developed/optimized test solutions for automotive serial link and battery management ICs on various automatic test equipment (ATE) platforms.
- Migrated high-volume products from constrained platforms/equipment into more cost-effective solutions, resulting in roughly 50 million USD in cumulative capex avoidance, 180% increase in throughput per product on average over the past 5 years.
- Wrote scripts and tools to automate various development tasks.
- Developed and facilitated ATE trainings for other test engineers.